

Board-Module connecting plug pair							
	Module: TE0715			Mapping	Base Board: TE0703		
	Connector: JM1				Connector: JB1		
	Connector Pins: 100				Connector Pins: 100		
XDC-Export	FPGA Pin	Schem. Name	PIN Number		Pin Number2	Comp. Pin / Name	Side
No		VIN	1	↔	2	3.3V	1
No		VIN	3	↔	4	3.3V	1
No		VIN	5	↔	6	3.3V	1
No		NOSEQ	7	↔	8	U5-78	1
No		VCCIO13	9	↔	10	VCCIO35	1
No		VCCIO13	11	↔	12	VCCIO35	1
No		3.3VIN	13	↔	14	3.3V	1
No		3.3VIN	15	↔	16	3.3V	1
No		MIO45	17	↔	18	U2-3	1
No		MIO44	19	↔	20	U2-1	1
No		MIO43	21	↔	22	U2-7	1
No		MIO42	23	↔	24	U2-6	1
No		MIO41	25	↔	26	U2-4	1
No		MIO40	27	↔	28	U2-9	1
No		GND	29	↔	30	GND	1
Yes	W12	B13_L3_P	31	↔	32	J1-A19	1
Yes	W13	B13_L3_N	33	↔	34	J1-A18	1
Yes	U11	B13_L5_P	35	↔	36	J1-C17	1
Yes	U12	B13_L5_N	37	↔	38	J1-C16	1